

E3 S1					
Day	8.30-10.00	10.00-11.30	11.30-1.00	2.00-3.30	3.30-5.00
Monday	VLSI(L1)	CEDM(	CEDM	VLSI(T1)	
Tuesday		ICT	ICT		
Wednesday		VLSI			VLSI
Thursday					
Friday	CEDM	ICT		CEDM	ICT
Saturday				CEDML	CEDML